



PATENT

B/9
12/19/01
[Signature]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Express Mail No.: EL 521380603US

Applicant : Chris L. Hoogenboom, et al.
Application No. : 09/006,777
Filed : January 14, 1998
Title : ATM SWITCH WITH RATE-LIMITING
CONGESTION CONTROL

Grp./Div. : 2663
Examiner : T. Kwoh

Docket No. : 39884/SAH/X2

RECEIVED
DEC 13 2001
Technology Center 2600

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Post Office Box 7068
Pasadena, CA 91109-7068
November 13, 2001

Commissioner:

In response to the Office action of August 10, 2001, please amend the above-identified application as follows:

In the Specification:

On Page 13, beginning on line 7, please delete and replace the entire first full paragraph as follows:

A DIBOC-based ATM switch in accordance with the more preferred embodiment of the invention advantageously implements priority/port-based and a connection-based congestion control strategy. For that purpose, output control 580 must know the present backlog of data units buffered in the P stores in data buffer 550 corresponding to output port 540, and must know more generally backlog in the I x P stores in the aggregate of data buffers corresponding to output port 540. Output control 580 therefore has I x P physical memories. Each